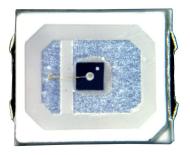


# Technical Data Sheet SMD Phototransistor: PT6724C

#### ■ Features

- . Fast response time
- . High photo sensitivity
- . Pb free
- . The product itself will remain within RoHS compliant version.



#### Descriptions

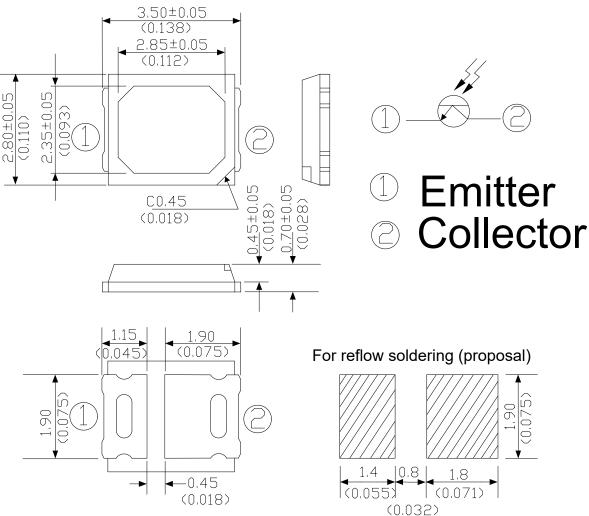
PT6724C is a high speed and high sensitive NPN silicon epitaxial planar phototransistor in a miniature flat top view lens SMD package. Due to its water clear epoxy the device is sensitive to visible and infrared radiation.

## Applications

- . Infrared applied system
- . Security system
- . Camera
- . Optoelectronic switch

PT6724C

#### Package Dimensions



Note: 1. All dimensions are in millimeters(inches)

- 2. Tolerances unless dimensions ± 0.1mm(.004")
- 3. Suggested pad dimension is just for reference only Please modify the pad dimension based on need

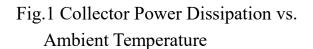
Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Units				
Collector-Emitter Voltage	Vceo	30	V				
Emitter-Collector-Voltage	VECO	6.5	V				
Collector Current	Ic	20	mA				
Power Dissipation at (or below) 25°C Free Air Temperature	Рс	75	mW				
Lead Soldering Temperature	Tsol	260	°C				
Operating Temperature	Topr	-20 ~ +85	°C				
Storage Temperature	Tstg	$-40 \sim +85$	°C				

Parameter	Symbol	Condition	Min.	Typ.	Max.	Units
Rang of Spectral Bandwidth	λ <sub>0.5</sub>		400		1100	nm
Wavelength of Peak Sensitivity	λp			940		nm
Collector – Emitter Breakdown Voltage	BV <sub>CEO</sub>	$I_{C}=100\mu A$ Ee=0mW/cm <sup>2</sup>	30		100	V
Emitter-Collector Breakdown Voltage	BV <sub>ECO</sub>	$I_E=100\mu A$ Ee=0mW/cm <sup>2</sup>	6.5			V
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =2mA Ee=1mW/cm <sup>2</sup>			0.3	V
Rise Time	tr	V <sub>CE</sub> =5V I <sub>C</sub> =1mA		15		μS
Fall Time	t <sub>f</sub>	RL=1000Ω		15		μS
DC Current Amplification Factor	H <sub>FE</sub>	V <sub>CE</sub> =5V, IC=2mA	1000		1800	
Collector Dark Current	Iceo	Ee=0mW/cm <sup>2</sup> V <sub>CE</sub> =20V			100	nA
On State Collector Current	I <sub>C(on)</sub>	$Ee=1mW/cm^2$ V <sub>CE</sub> =5V	0.5	1.5		mA

# ■ Electro-Optical Characteristics (Ta=25°C)

# **Typical Electro-Optical Characteristics Curves**



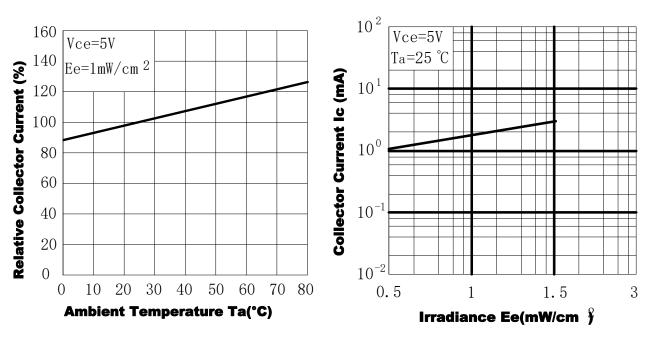
Relative Spectral Sensitivity(%) Collector Power Dissipation (mW) Ta=25° -40 -20 **Ambient Temperature Ta(°C)** Wavelength(nm)

Fig.2 Spectral Sensitivity

#### Fig.3 Relative Collector Current vs.

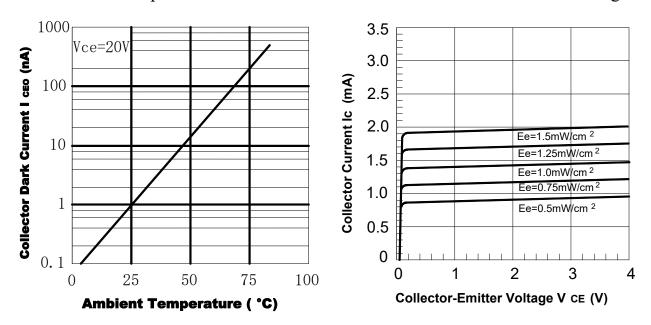
#### Ambient Temperature

# Fig.4 Collector Current vs. Irradiance



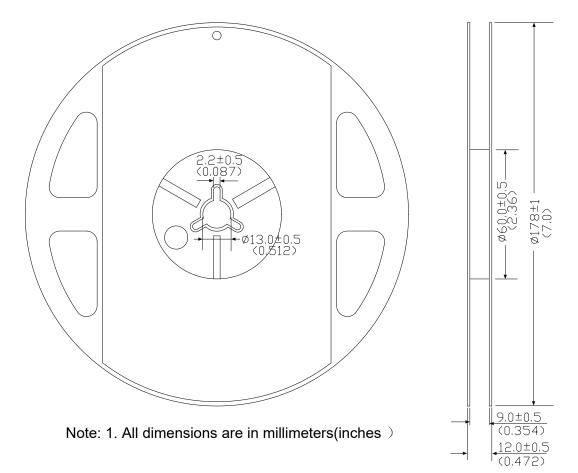
# Fig.5 Collector Dark Current vs. Ambient Temperature

# Fig.6 Collector Current vs. Collector-Emitter Voltage

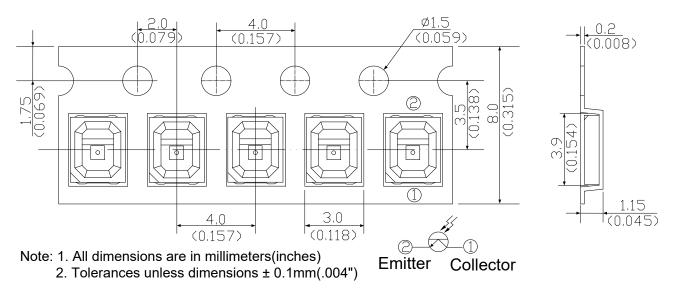




#### Packing Dimensions



## **Carrier Tape Dimensions (Quantity: 4000 pcs/reel)**



#### Notes

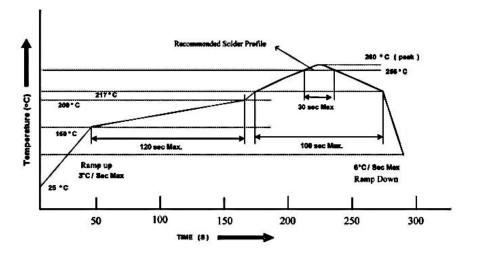
- 1. Above specification may be changed without notice. SHUGUAN will reserve authority on material change for above specification.
- 2. Before using this product, be sure to test it. The use and storage conditions must not exceed the SHENZHEN SHUGUAN ELECTRONIC TECHNOLOGY CO.,LTD. V1.0 2020.04.23

limit parameters specified in this manual. The company will not be responsible for any damage to the product caused by the use of the product beyond the limit parameters.

- 3. Stored at a temperature not higher than 30° C and humidity not higher than 60%RH, the product shelf life is 6 months. Keeping the product in an airtight container with a desiccant can extend the shelf life of the product to some extent. Poor storage conditions can cause corrosion of product leads or changes in product performance.
- 4. After opening, the product must be used within 168 hours (recommended working environment temperature not higher than 30 °C, humidity not higher than 60%). If it is not used up, the remaining material must be stored in an environment where the temperature is not higher than 30° C and the humidity is not higher than 10%.
- 5. For products that have not been soldered, if the hygroscopic agent or packaging fails, or the product does not meet the above valid storage conditions, baking can play a certain performance recovery effect. Baking conditions:  $60\pm5^{\circ}$ C, duration 96H.
- 6. Static electricity and surges will cause changes in product characteristics, such as forward voltage reduction, etc. If the situation is serious, it will even damage the product, so effective anti-static measures must be taken during use. All related equipment and machines should be properly grounded, and other measures against static electricity and surges must be taken. The use of anti-static wristbands, anti-static mats, anti-static work clothes, work shoes, gloves, and anti-static containers are all effective measures to prevent static electricity and surges.
- 7. When designing the circuit, the current passing through the product cannot exceed the specified maximum value, and a current limiting resistor must be used at the same time, otherwise a small voltage change will cause a large current change, which may lead to product damage.
- 8. Welding should pay special attention to:
  - (1) Manual soldering: the tip temperature of the soldering iron (up to 25W) should not exceed  $350^{\circ}$  C; the soldering iron must be grounded, and the static electricity should not exceed the range; the soldering time should not exceed 3 seconds.
  - (2) Reflow soldering

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. Pb-free solder temperature profile



- . Reflow soldering should not be done more than two times.
- . When soldering, do not put stress on the phototransistor during heating
- . After soldering, do not wrap the circuit board.